

What Does The Future Hold for Hypervisor Security?

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Royal Holloway, University of London, UK, September 12th, 2013.*

Major Evolutions in IaaS Architecture Ahead!

▶ **Virtualization:**

- Fuels growth of cloud computing...
- ...but raises many security concerns.

▶ **Architecture is fundamental for IaaS security...**

▶ **... But hypervisor architecture is changing rapidly!**

- New hypervisor architectures are defined to mitigate new threats.
- Virtualization is expanding outside the data center.



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▶ ... But hypervisor architecture is changing rapidly!

- New Are current architectures addressing upcoming threats?
- Virtual What is the overall view of such evolutions?



Major Evolutions in IaaS Architecture Ahead!

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▶ **... But hypervisor architecture is changing rapidly!**

- New hypervisor architectures are defined to mitigate new threats.
- Virtualization is expanding outside the data center.

▶ **Contributions:**

1. Identify some major disruptions shaping up the future of hypervisor security.
2. Abstract hypervisor evolution into a consistent roadmap.
3. Give an overview of challenges, benefits, limitations of each architectural approach.

- ▶ **A Big Picture.**
- ▶ **Trend #1: Extension to Embedded Systems.**
- ▶ **Trend #2: Migration of Security Towards the Hardware.**
- ▶ **Trend #3: Evolution towards Multi-Clouds.**
- ▶ **Conclusion.**

A Big Picture



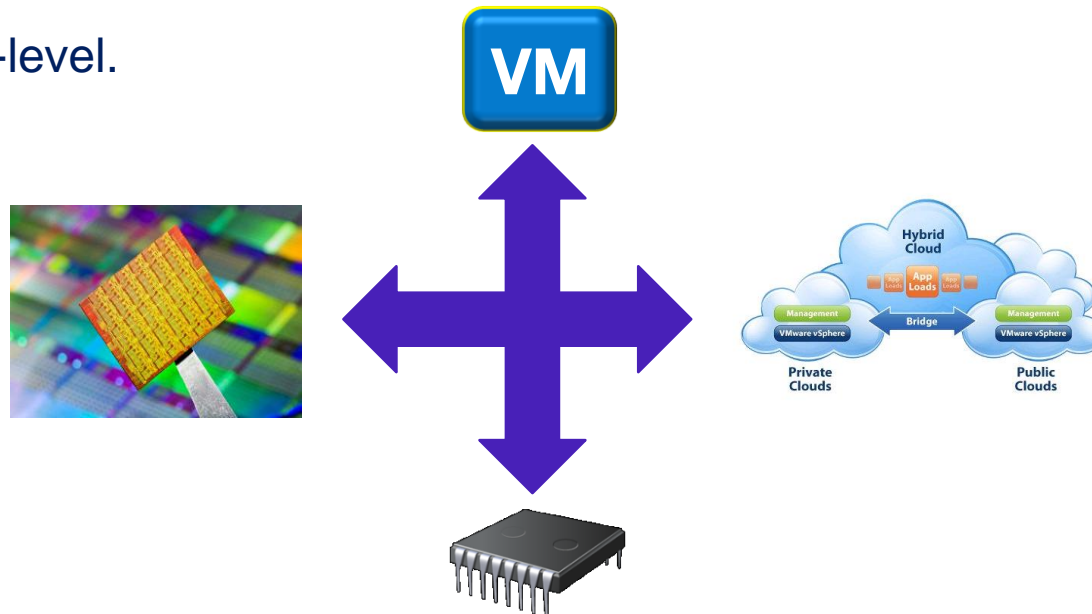
Changes in Hypervisor Security Architecture

▶ Some bottom-line technological trends:

- Availability of increasingly small-scale devices.
- Rising software complexity, commoditization of hardware for dedicated processing.
- Fall of barriers between virtualized systems, increasingly distributed.

▶ Two dimensions in change:

- Scale.
- Abstraction-level.



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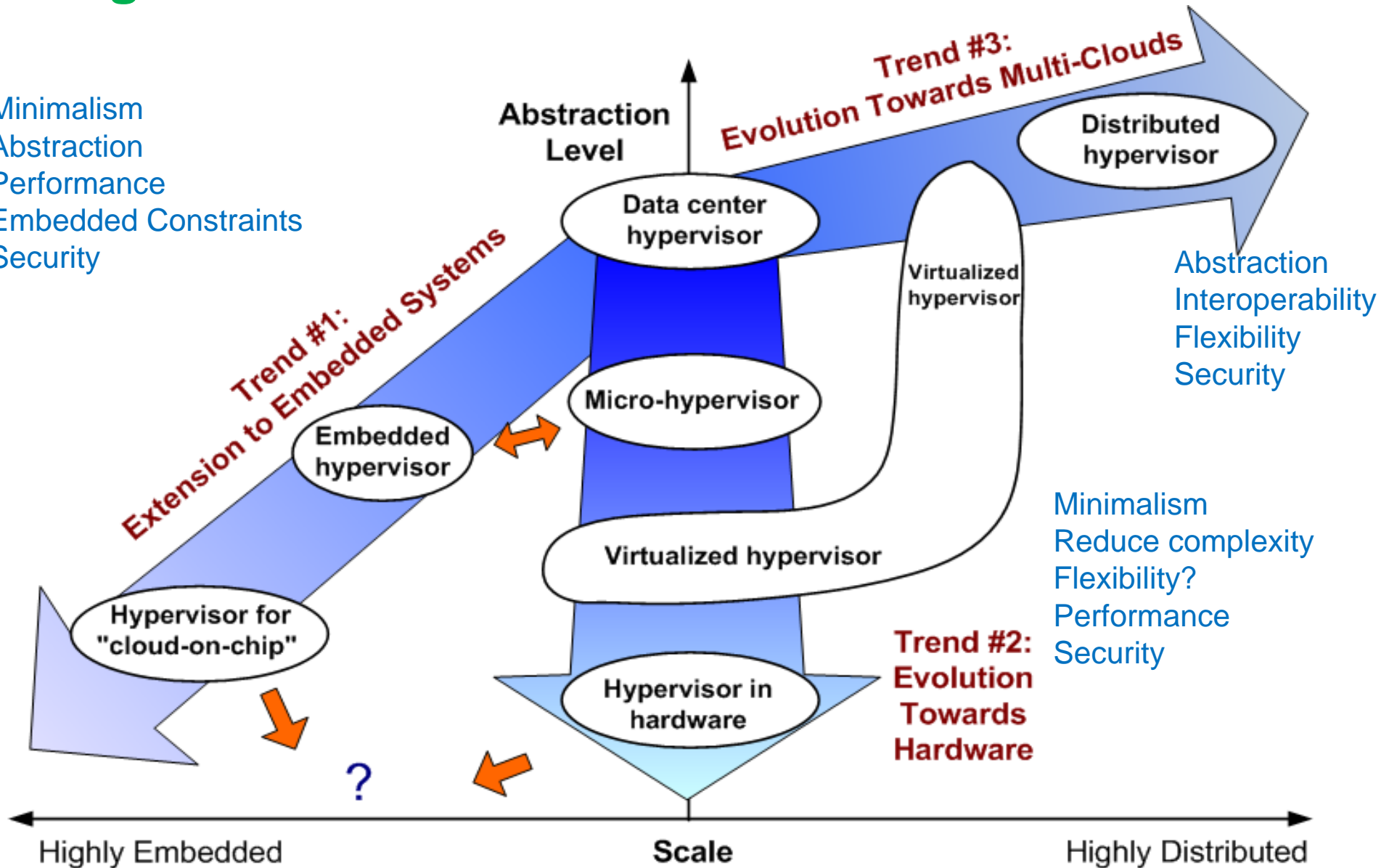
- Scale.
- Abstraction-level.

Three main trends

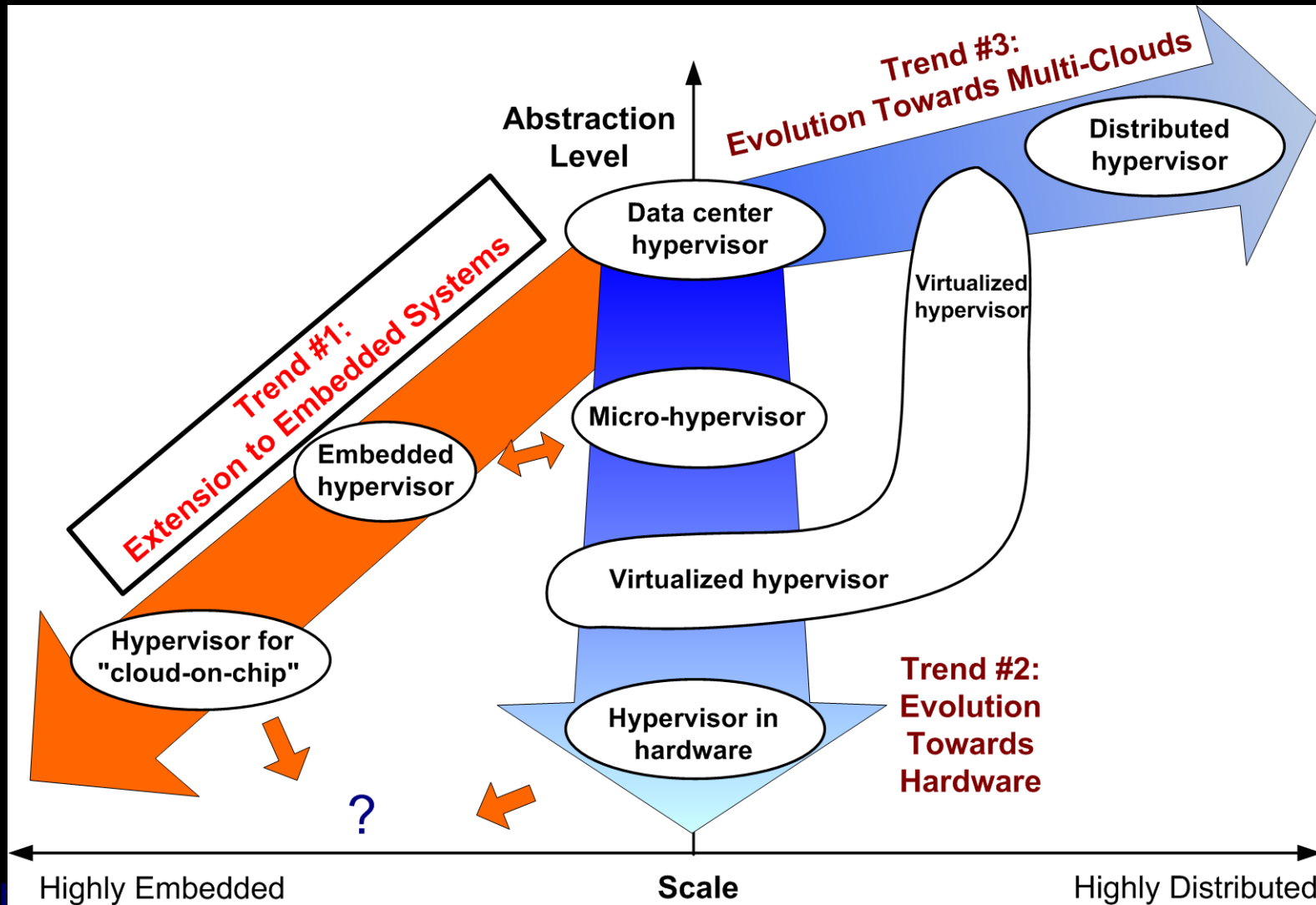
1. Virtualization goes embedded.
2. Security moves towards the hardware.
3. The cloud becomes user-centric.

A Big Picture

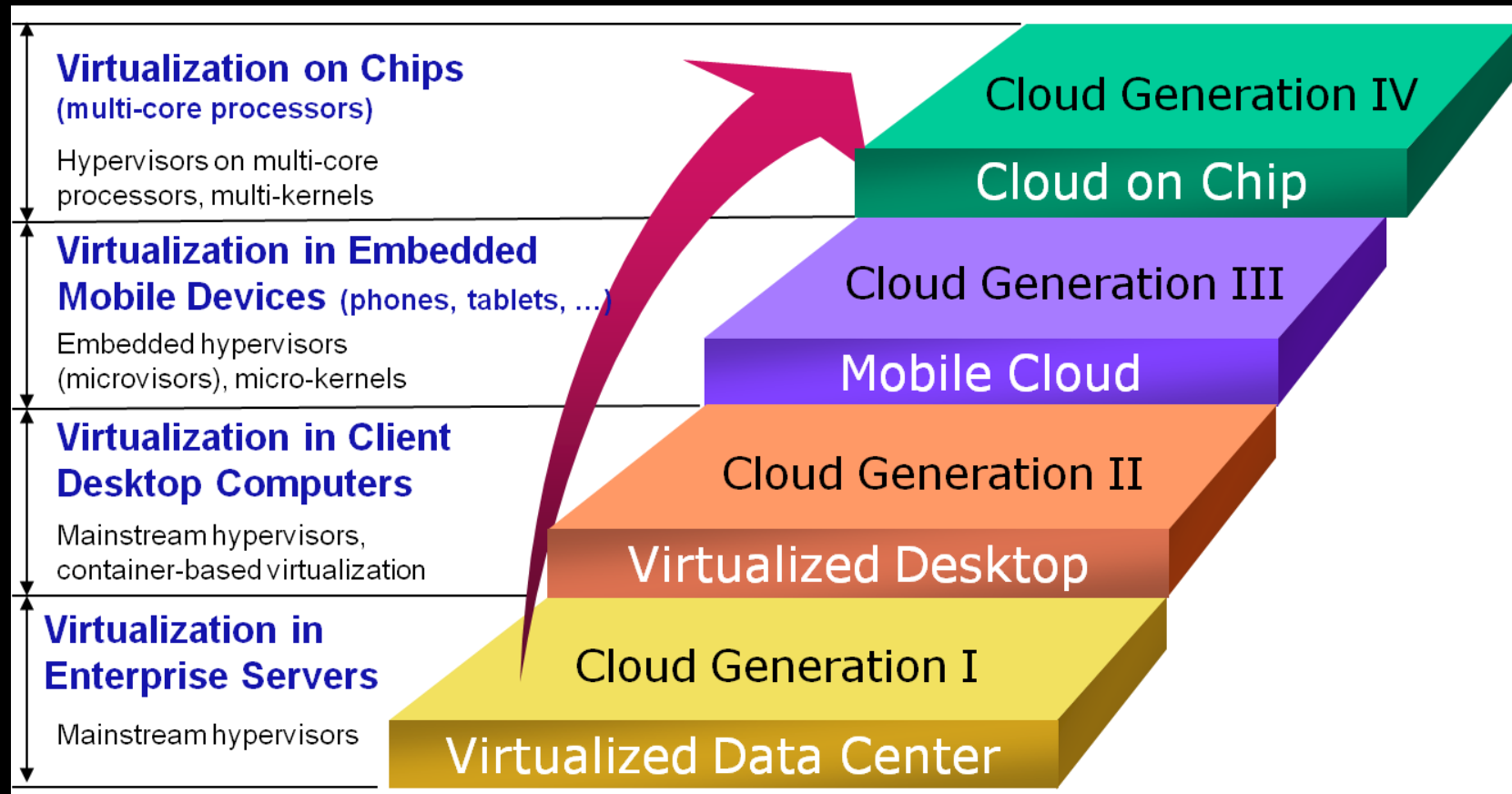
Minimalism
Abstraction
Performance
Embedded Constraints
Security



Disruption #1: Virtualization Goes Embedded



Disruption #1: Virtualization Goes Embedded



Embedded Hypervisors

Embedded systems features

Rising complexity

Expanding code size

Heterogeneous sub-systems

Hardware diversity Open architectures

Feature-rich platforms

Security issues



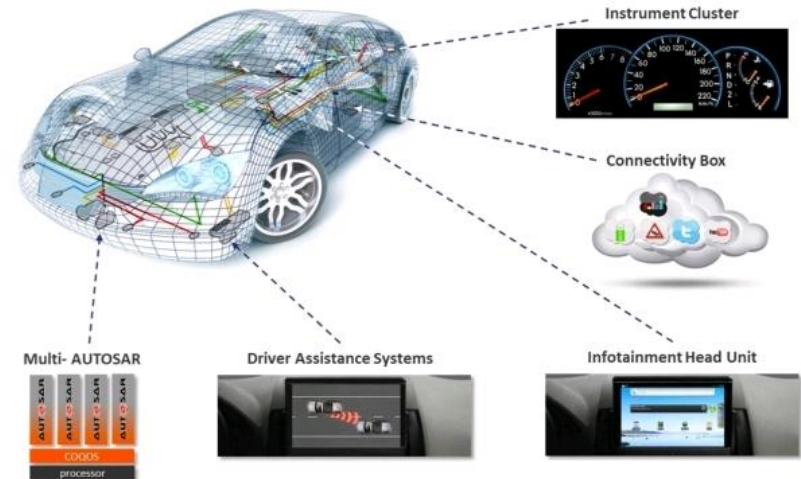
Key design challenges

- **Resource abstraction:** overcome resource heterogeneity (multicore support, multiple OSes on same platform...).
- **Isolation:** contain faults/attacks between sub-systems.
- **Performance:** efficient inter- sub-system communication.
- **Minimal TCB:** reduce attack surface, strong assurance.
- **Real-time guarantees:** unique scheduling control point.
- **Modularity:** facilitate code reuse in open ecosystems.
- **Fine-grained resource control:** unique control point of security policy enforcement

DC Hypervisor

Embedded Hypervisor

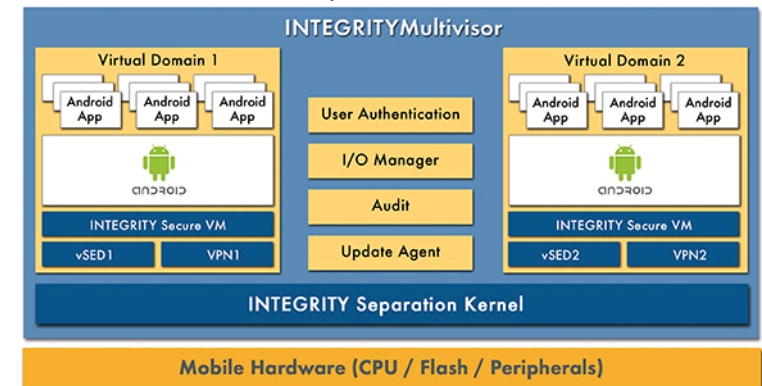
Cloud-on-chip hypervisors



Source: OpenSynergy, COQOS platform.



Source: N. Navet, B. Delord, M. Baumeister. Virtualization in Automotive Embedded Systems: an Outlook, ERTS 2010.



Source: GreenHills software, Integrity multivisor.

Embedded Hypervisors

Which Architecture?

- **Hypervisors** have strong limitations.
- **Micro-kernels** seem better suited.
- **Micro-visors** might be even better...

Traditional hypervisors

VM multiplexing, isolation
May be improved (vSwitch)
Huge TCB
2-level scheduling
Complexity of driver sharing
Heavyweight VMs



Key properties

Resource abstraction
Isolation
Performance
Minimal TCB
Real-time guarantees
Modularity
Fine-grained control

Micro-kernels

? Increasing virtualization support
+ Strong isolation
+ Efficient IPCs
+ Extremely minimal kernel
+ Well-established RTOS approach
+ Flexible driver sharing patterns
+ Lightweight threads

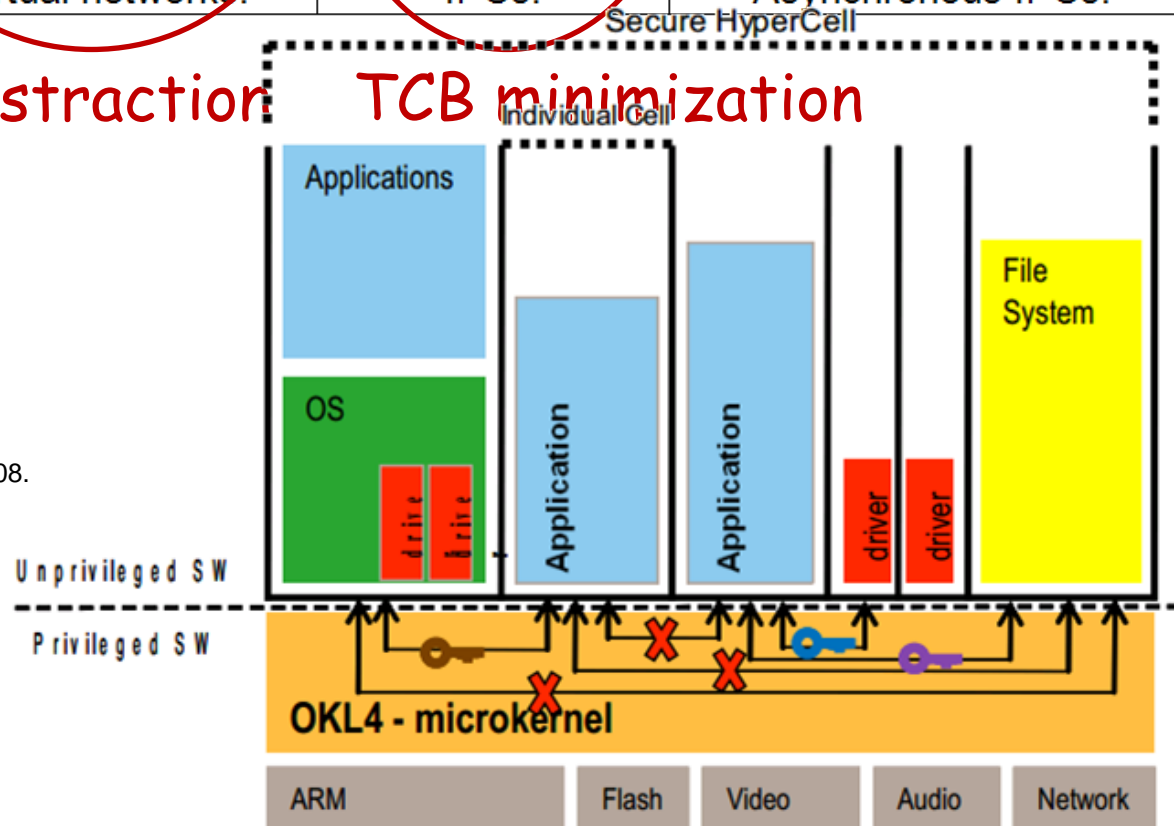
Microvisor Architectures

▶ **Microvisor = convergence of hypervisors and micro-kernels:**

Architecture	Hypervisor	Micro-kernel	Micro-visor
Execution model	VM, vCPU.	Threads.	vCPU.
Memory	vMMU.	Address space	vMMU.
I/O	Virtual device drivers in VM or hypervisor	User mode drivers.	User mode drivers. Virtualized interrupts.
Communication	Virtual networks.	IPCs.	Asynchronous IPCs.

Abstraction TCB minimization

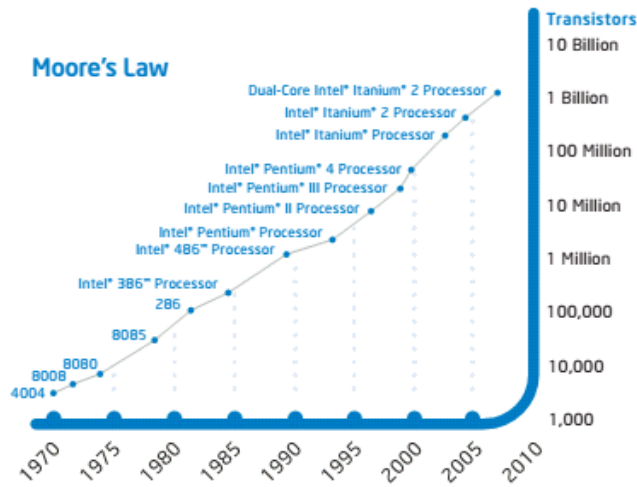
▶ **OKL4 architecture:**



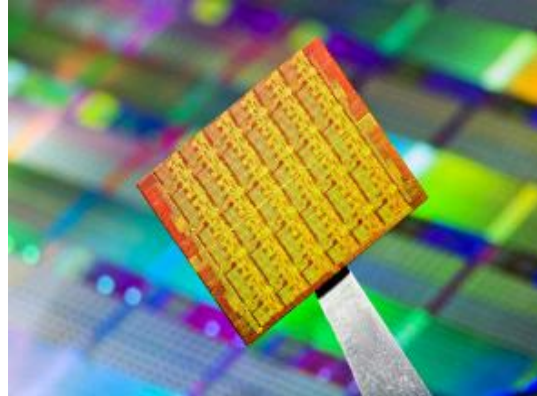
Source: J. Matthews. Virtualization and Componentization in Embedded Systems. Open Kernel Labs Technology White Paper, 2008.

Towards the Cloud-on-Chip

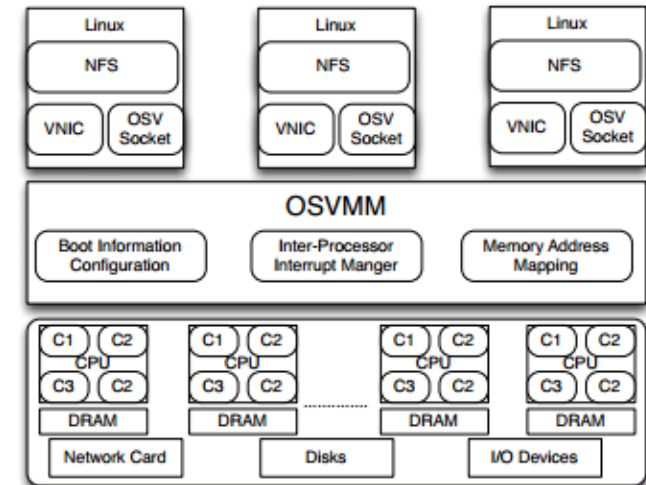
Hypervisors for multi-core architectures



Source: Intel.



Source: Intel.



Source: Y. Dai et al. A Lightweight VMM on Many Core for High Performance Computing, *VEE 2013*.

Key challenges

- **Resource sharing limitation.**
 - Poor physical isolation (memory, storage, CPU, I/O).
 - Failure/attack propagation.
- **Massive scalability.**
 - Hyperscale server consolidation.
 - Synchronization.
 - Fair resource allocation.



Single hypervisor for multi-cores

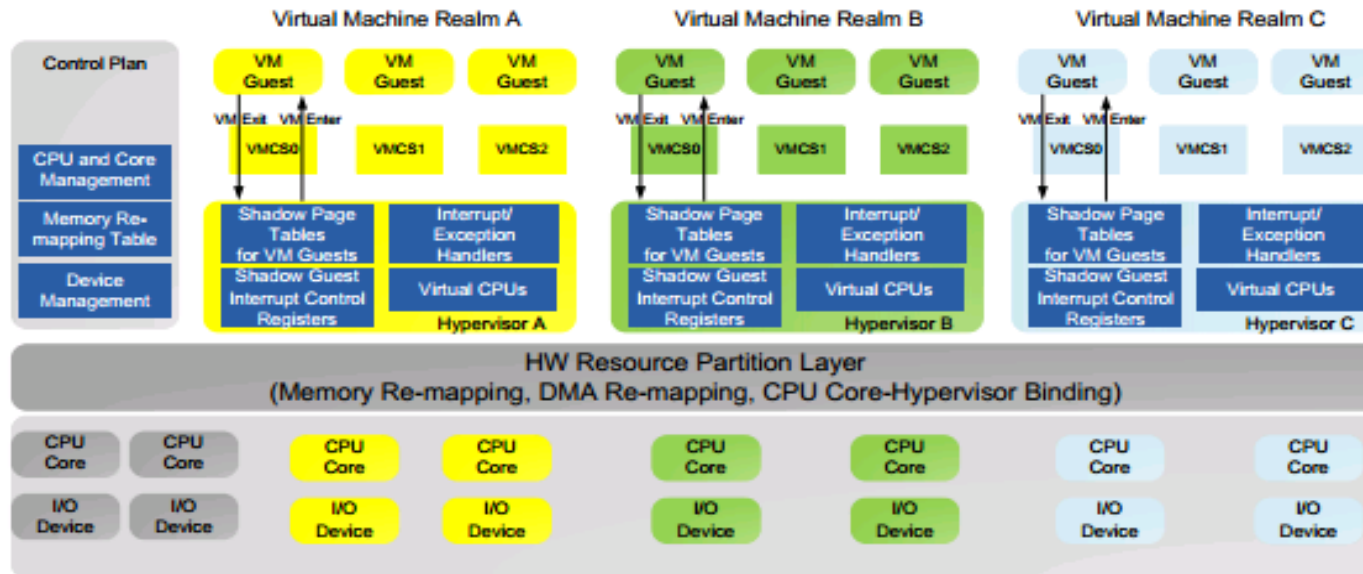
- **Multi-core management in guest OS:** strong scalability restrictions.
- **Multi-core management in hypervisor:** scalability and security limitations, e.g.,
 - Risk of resource starvation.
 - System-wide hypervisor state sharing.
 - Hypervisor = single point of failure.
 - Hypervisor vulnerabilities poorly confined.

Towards the Cloud-on-Chip

DC Hypervisor

Embedded Hypervisor

Cloud-on-chip hypervisors



Source: W. Shi. Architectural Support of Multiple Hypervisors over Single Platforms for Enhancing Cloud Computing Security. *ACM International Conference on Computing Frontiers (CF)*, 2012.

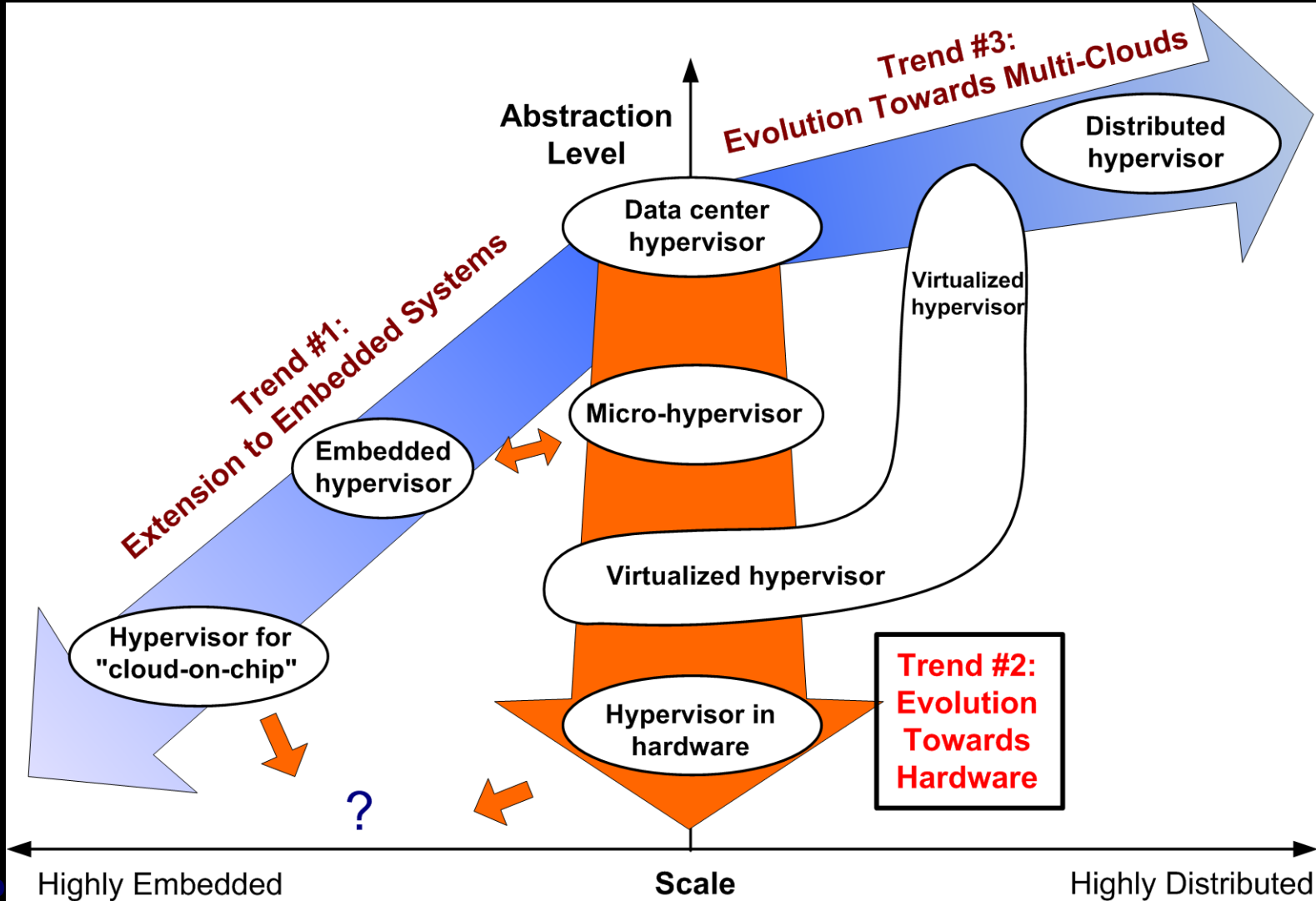
Multiple hypervisors on same chip

- Independent **security realms**
 - per hypervisor,
 - with dedicated cores and memory.
- Two-level resource management:
 - *Intra-hypervisor* for VMs.
 - *Inter-hypervisor* using multiplexing HAL.

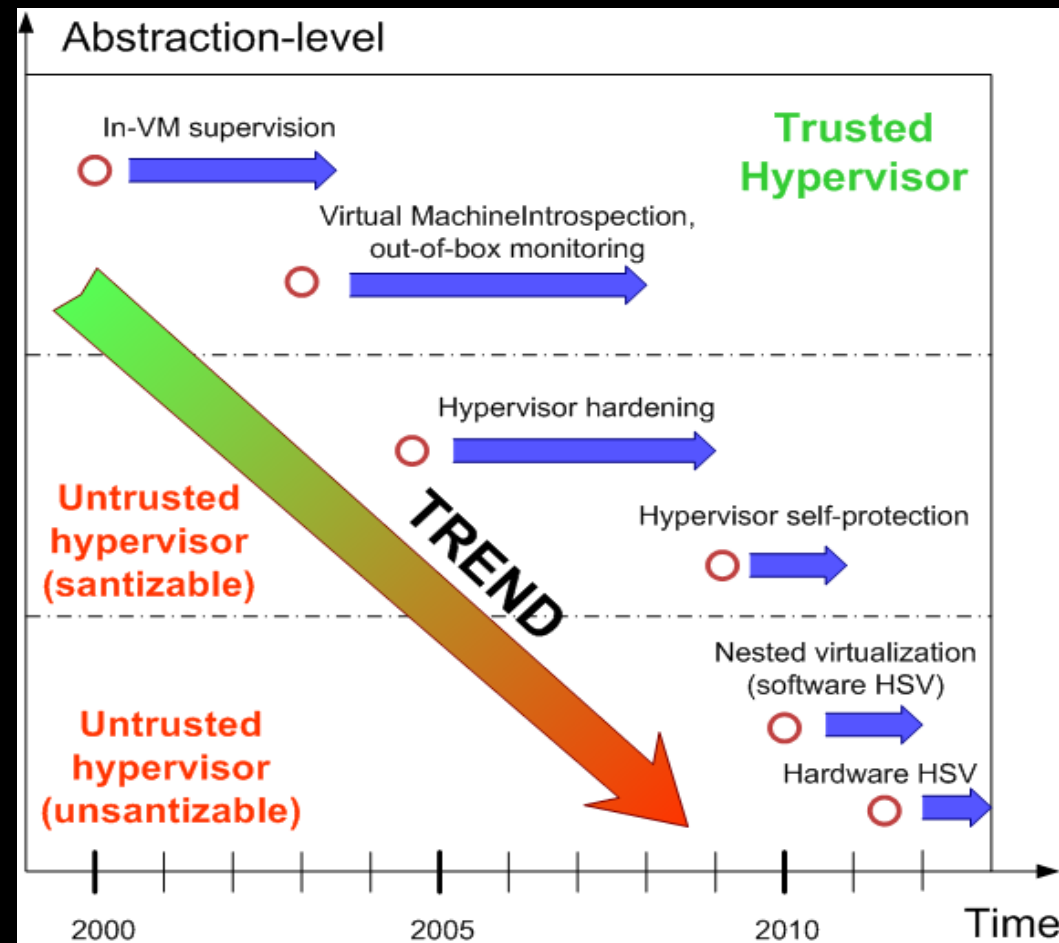
Benefits

- **Increased resilience:**
 - Avoid platform-wide bug/attack propagation through realm confinement.
- **Better scalability:**
 - Hardware platform = distributed system.
 - Decentralize VMM functionalities for finer-grained control.

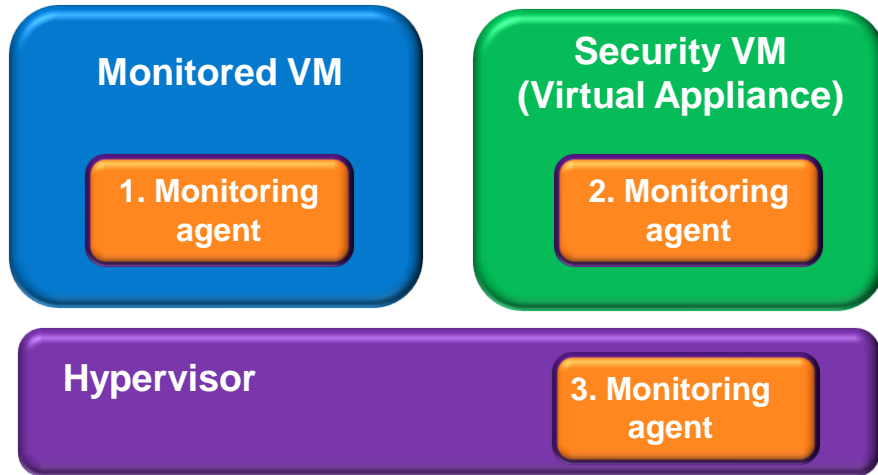
Disruption #2: Security Moves Towards the Hardware



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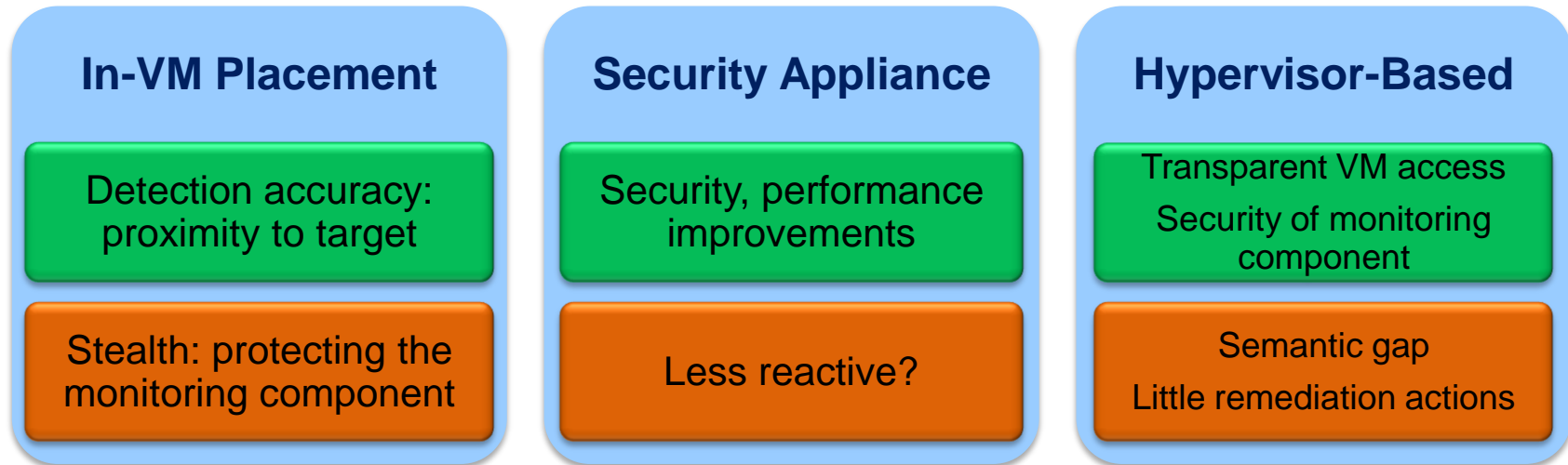
VM Introspection



VM Introspection Idea: use the capabilities of the hypervisor to supervise VM behaviors

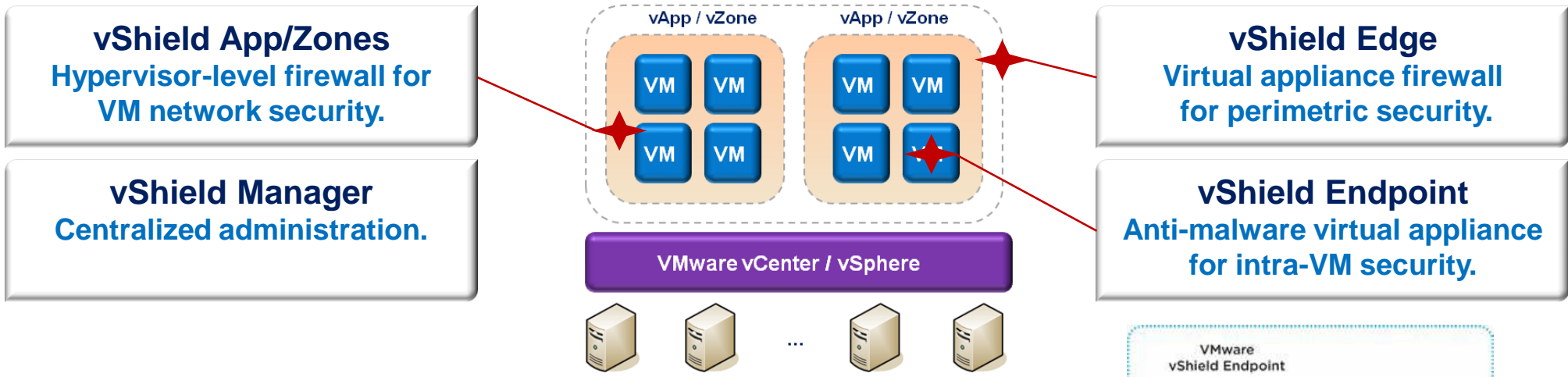
Some Systems	
1.	In-VM monitoring: SIM
2, 3.	With no hooks in VM: CloudSec
2,3.	With hooks in VM: Lares, XenAccess, KVMSec

Compute, network, storage introspection...
Fast path, slow path, hybrid path architectures...

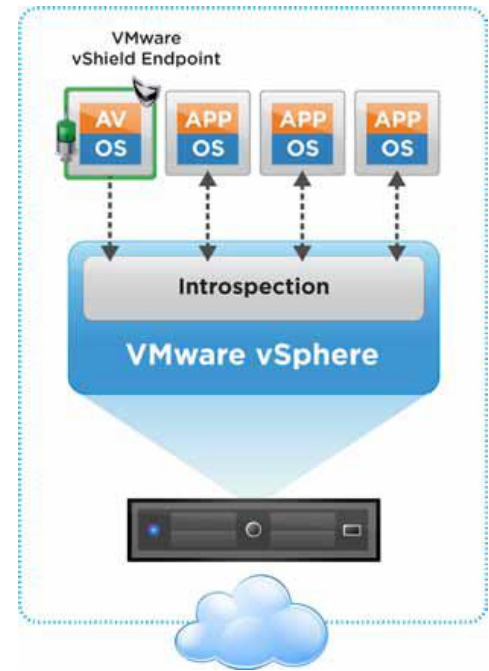


An Example

vShield = VMware's IaaS security suite

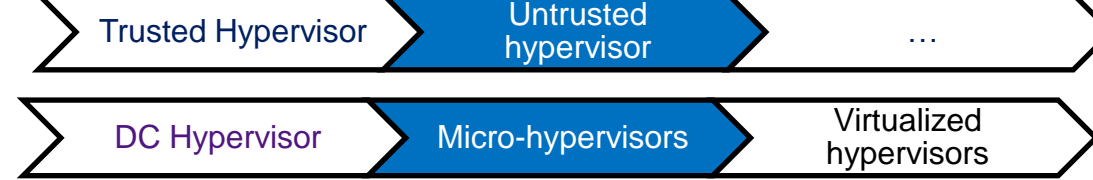


vShield Endpoint



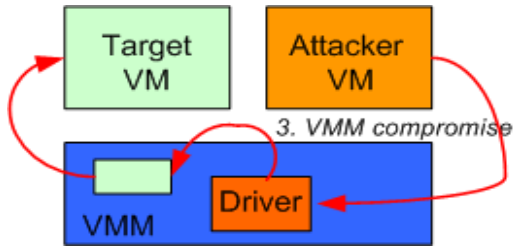
- ▶ **Security features:** anti-malware, integrity monitoring, firewall, Deep Packet Inspection (DPI), log inspection.
- ▶ **Policy-based management.**
- ▶ **Cross-layering:** module in hypervisor + security appliance.
- ▶ **Openness:** EPSec API.

Micro-Hypervisors



The problem

- Hypervisors are **too big, too complex**.
- Source of vulnerabilities: **bounce attacks**.



Solutions

- **TCB hardening:** mechanisms
Protect « by hand » hypervisor from subversion.
⇒ Trusted computing, language techniques, sandboxing...

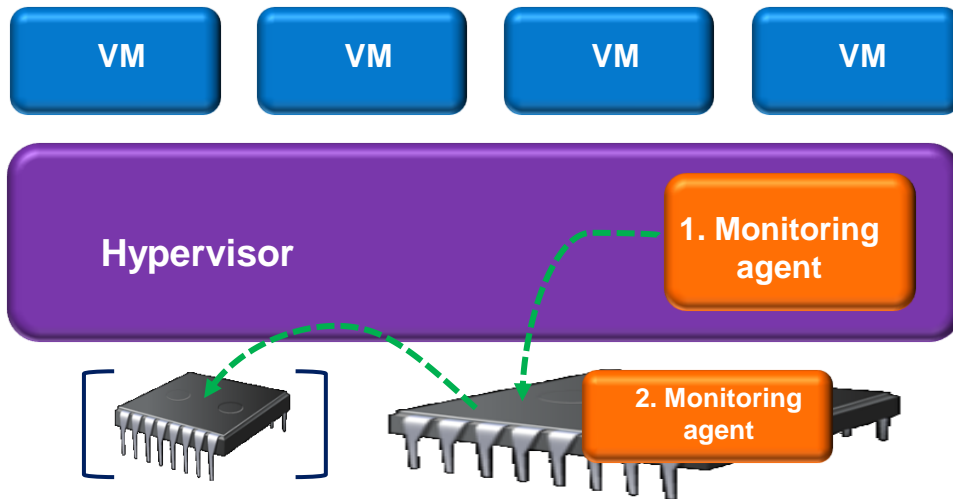
TCB Hardening: Trusted Computing Architectures

- **Security objective:** trustworthy VMM, with high assurance for **authenticity** and **integrity**.

Trusted computing technologies.

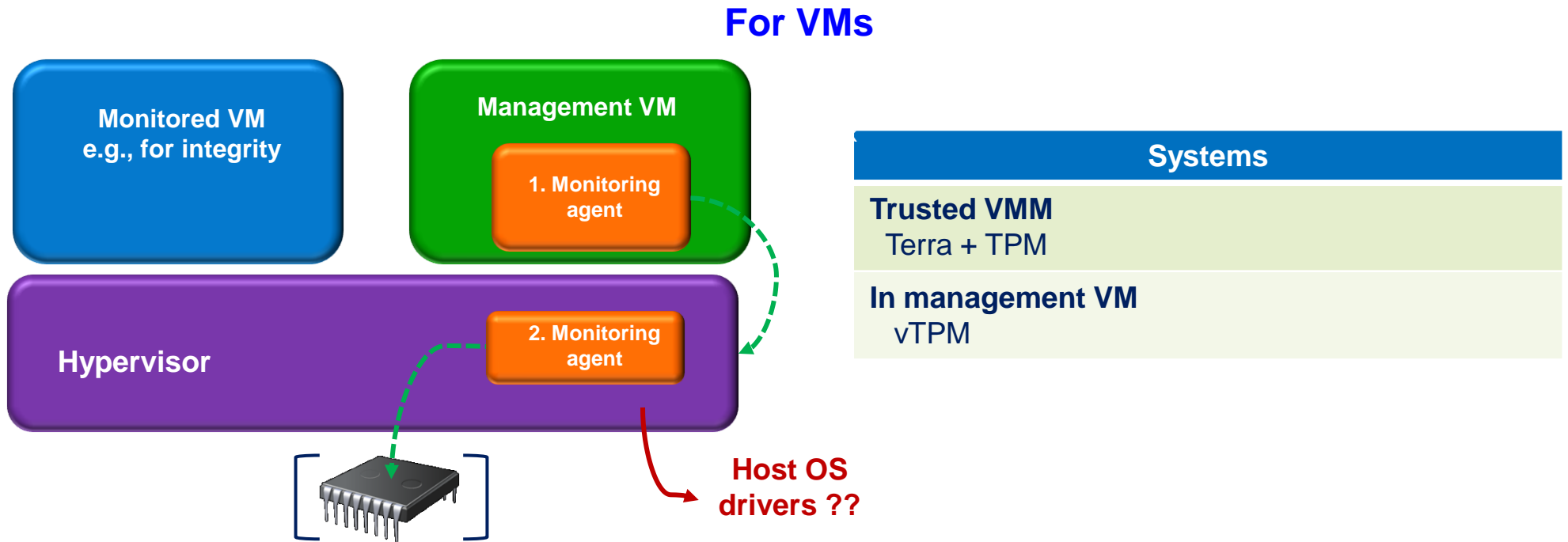
Provide attestation of integrity of software/hardware components relying on **chain of trust**.

For the Hypervisor

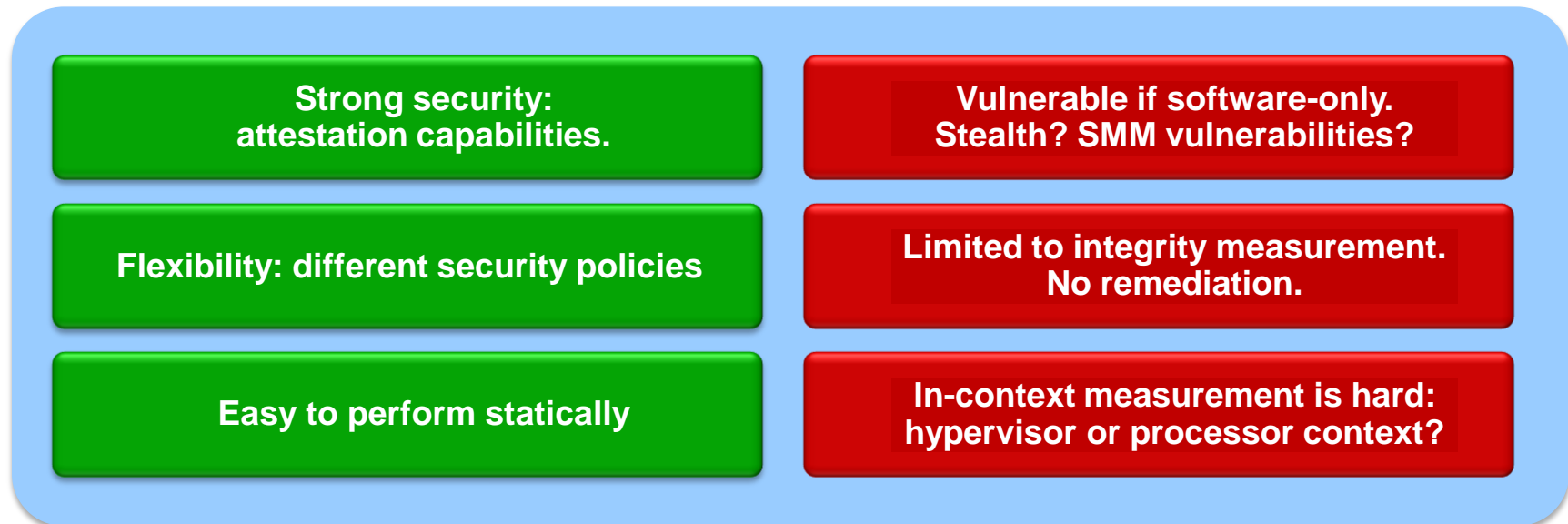


Systems
Integrity checking TCG IMA, Hyperguard, HyperCheck, HyperSentry
Control flow integrity HyperSafe

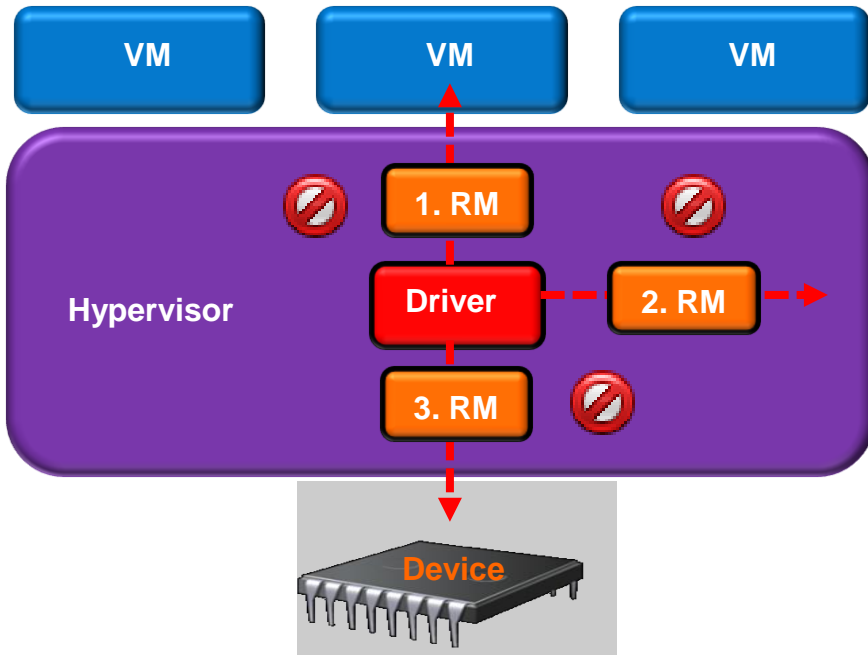
TCB Hardening: Trusted Computing Architectures



Benefits and Limitations



TCB Hardening: Driver Sandboxing



Idea: confine malicious code by controlling communications between driver, and device, kernel, and VM space.

Example of Systems

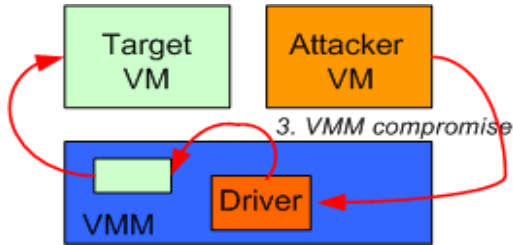
- 1. **Reference Monitor (RM) between driver / VM space:**
MicroDrivers, Proxos
- 2. **RM between driver and hypervisor:**
Software Fault Isolation (SFI) techniques
- 3. **RM between driver and device:**
Nooks

Strong security	RM difficult to protect without hardware mechanism
Good performance	No remediation, only containment
Reduced code size	Hypervisor is modified
Some isolation flexibility	Policies difficult to configure

Micro-Hypervisors

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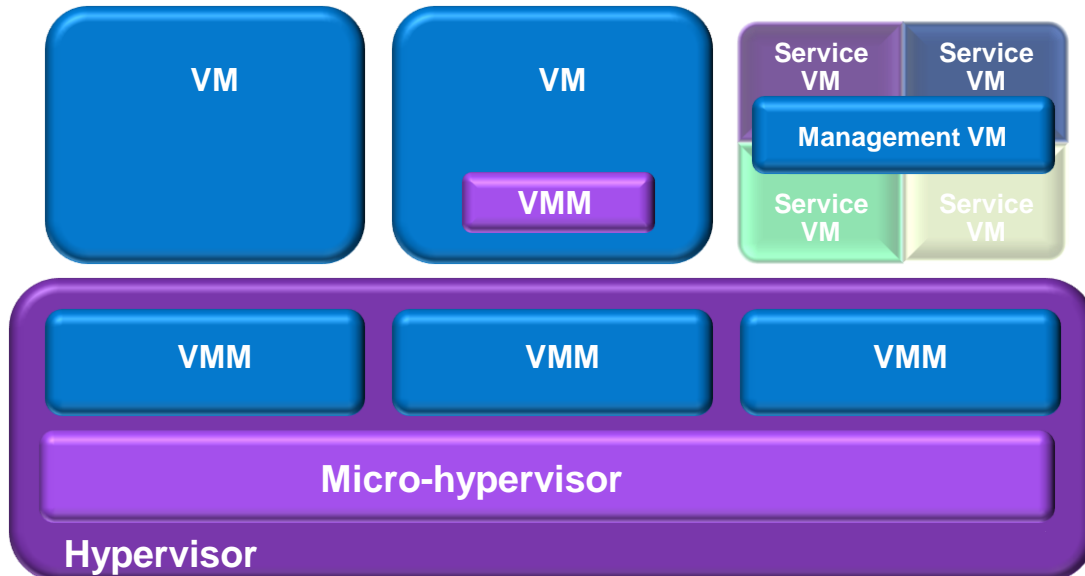
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- **TCB reduction:** architectures
Reduce code size and complexity and increase modularity.
 ⇒ For the **core hypervisor**: **Micro-hypervisors**.
 ⇒ For the **management VM**: **Disaggregated hypervisors**.

Reducing the TCB



Core hypervisor: virtualization iKernel (for drivers), NOVA, NoHype

Expel as much code as possible from TCB

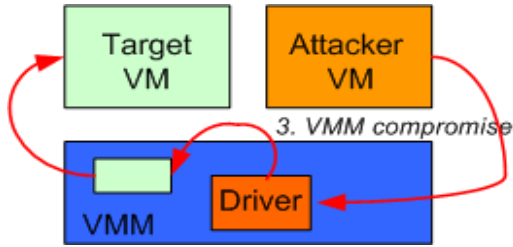
- ⊕ Strong security
- ⊕ Flexibility with open architecture.
- ⊖ Extensive code rewriting
- ⊖ Limited operational services
- ⇒ Hard to apply to legacy hypervisors.

Micro-Hypervisors



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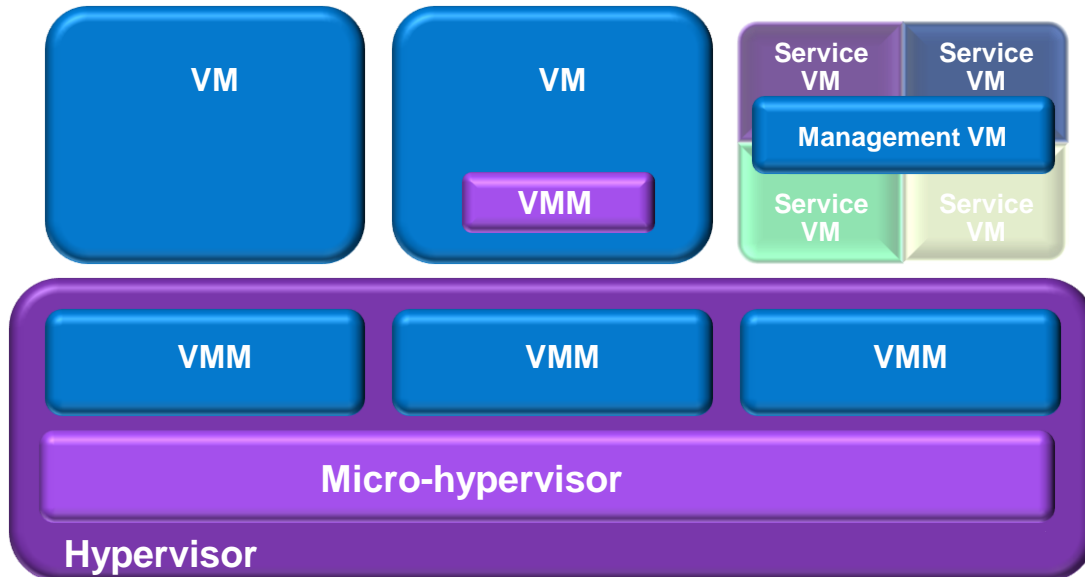
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Reducing the TCB

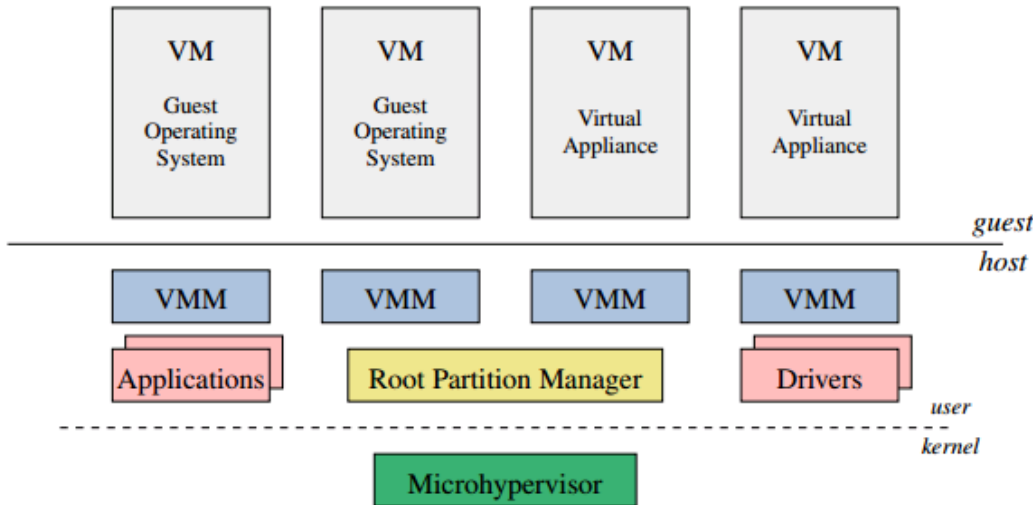
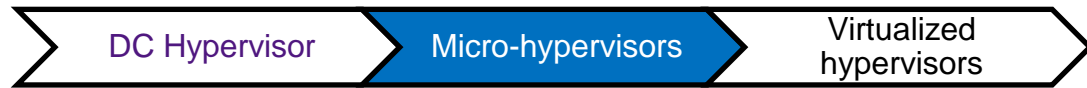


Management VM: componentization XOAR, MinV, Disaggregated Xen

Transform Dom0 into a set of service VMs, limiting resource sharing, reducing privileges.

- ⊕ Improved security, flexibility, and control.
- ⊕ Does not limit operational services.
- ⇒ More ready to apply to legacy hypervisors.

Some Examples



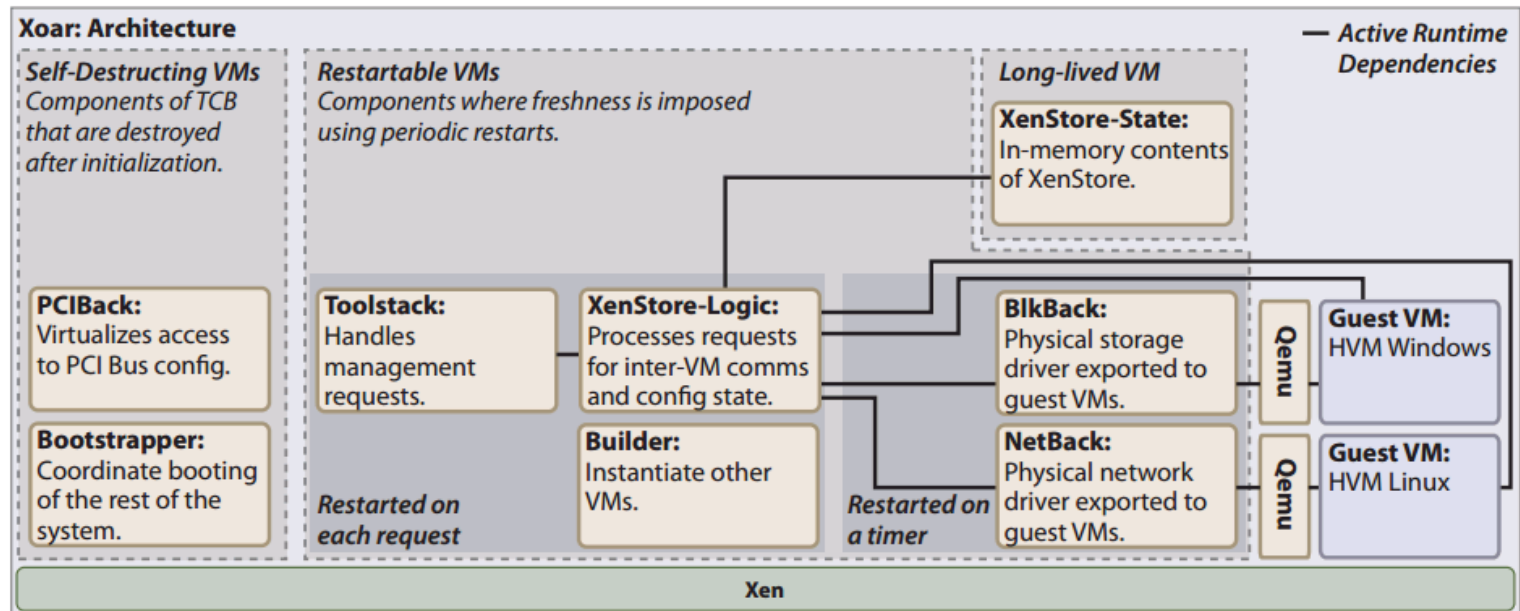
NOVA Architecture

Source: U. Steinberg and B. Kauer. NOVA: A Microhypervisor Based Secure Virtualization Architecture. EUROSYS 2010.

XOAR Architecture

Source: P. Colp et al. Breaking Up is Hard to Do: Security and Functionality in a Commodity Hypervisor. SOSP 2011.

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For Automated Hardening...

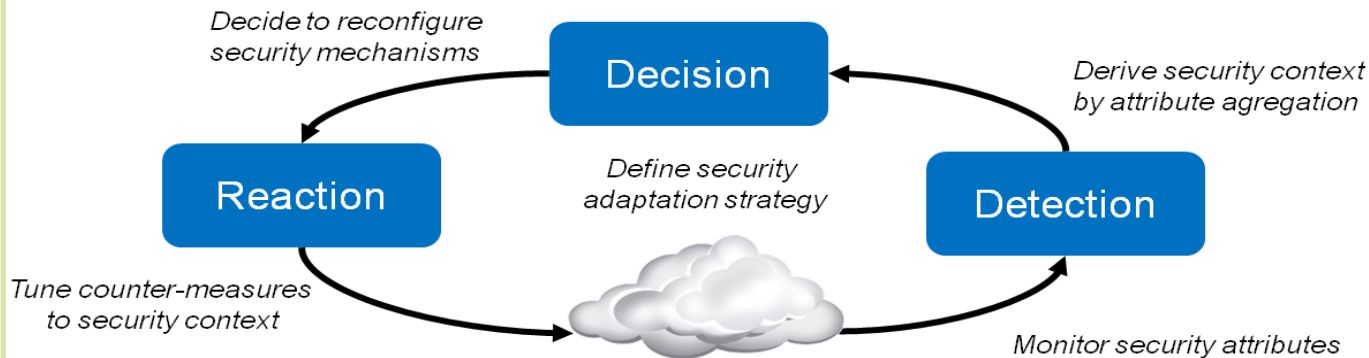
Some hard problems



security component **heterogeneity** between layers and domains.

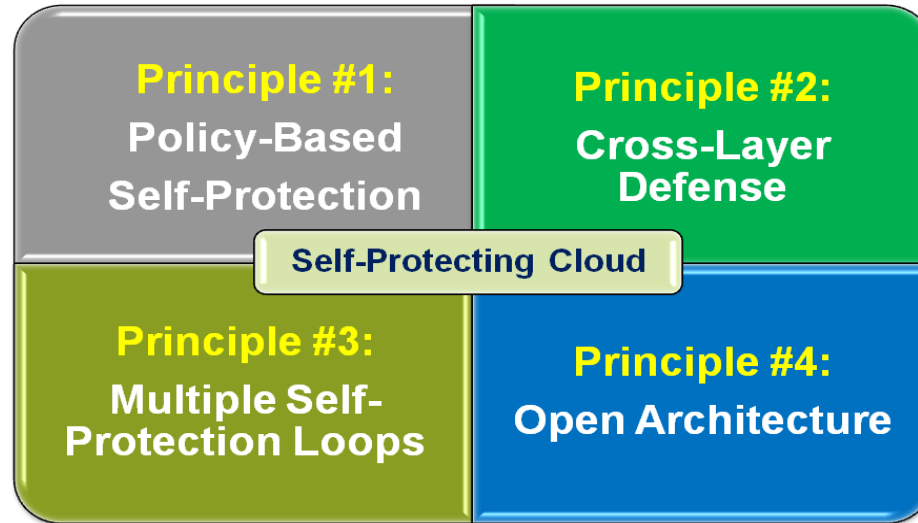
infrastructure **complexity** ⇒ impossibility of manual administration.

Autonomic security approach: clouds with self-defense capabilities



- Lighter administration.
- Increased reactivity.
- Lower operational costs.
- Graduated response.
- Security supervision enabler.

VESPA: Multi-Layer IaaS Self-Protection



▶  = **Virtual Environments Self-Protecting Architecture**

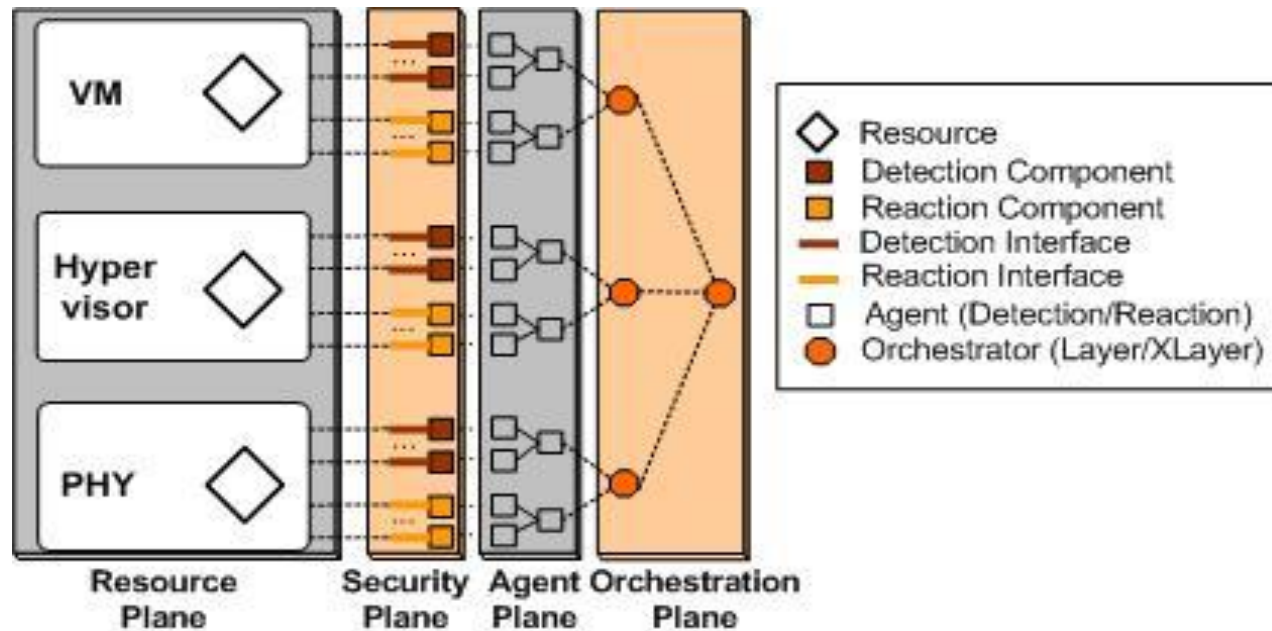
An autonomic security framework for regulating protection of IaaS resources.

▶ **Implementation:** KVM-based IaaS infrastructure.

▶ **Application to hypervisor self-protection:** in progress.



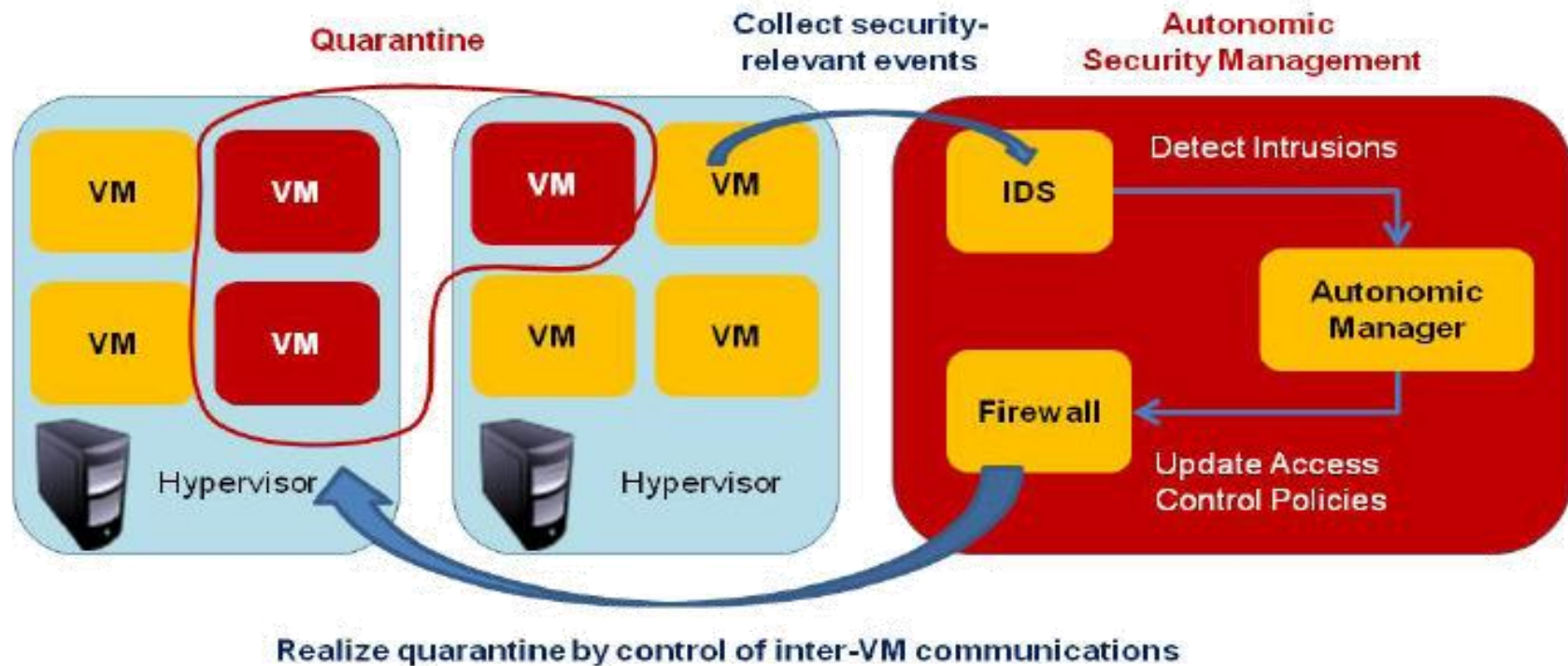
Example: The VESPA Framework



Key points

- VESPA: architecture for effective and flexible IaaS self-protection.
- Two-level tuning of security policies, within and across layers.
- Coordination of multiple loops for rich spectrum of defense strategy.
- Multi-plane open design for easy integration of detection/reaction COTS.

Flexible confinement of VMs according to risk level



The problem

IaaS infrastructures lack:

Vertically: security

- Untrustworthy, vulnerable layers.

Horizontally: flexibility, interoperability

- (Security) features not deployed.
- Too monolithic for customization.

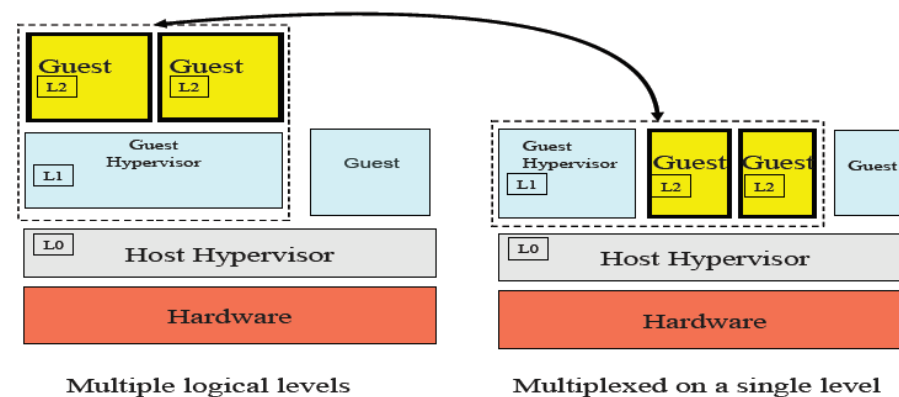
Virtualized Hypervisors

Idea: Virtualize the hypervisor

Hypervisor-Secure Virtualization (HSV):

- The hypervisor is no longer part of the TCB.
- Protection by a security layer underneath.
- Separation of resource management from security.

Software HSV approach: nested virtualization.



Source: IBM, Turtles project, OSDI'10.

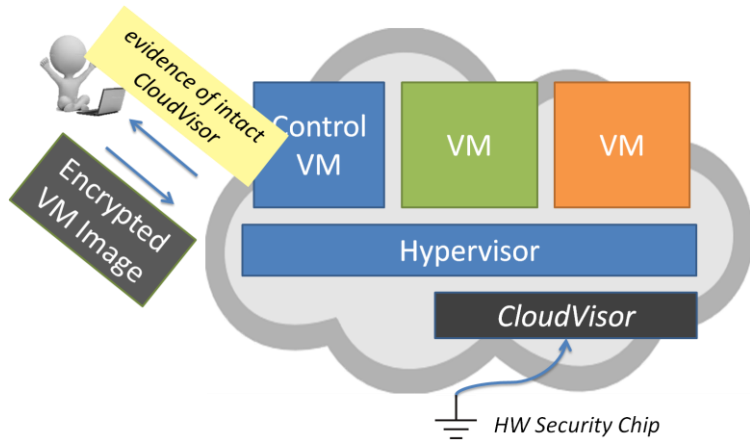
Benefits

Vertically: more security

- Trustworthy security layer.

Horizontally: more flexibility, interoperability

- Distributed security abstraction layer.
- Enabler for cross-provider security services.



Source: Zhang et al., CloudVisor, SOSP'11.

The Hypervisor in Hardware

Hardware HSV

A hardware controller as only security manager.

- Dedicated Page Ownership Tables for checking memory mapping permissions.

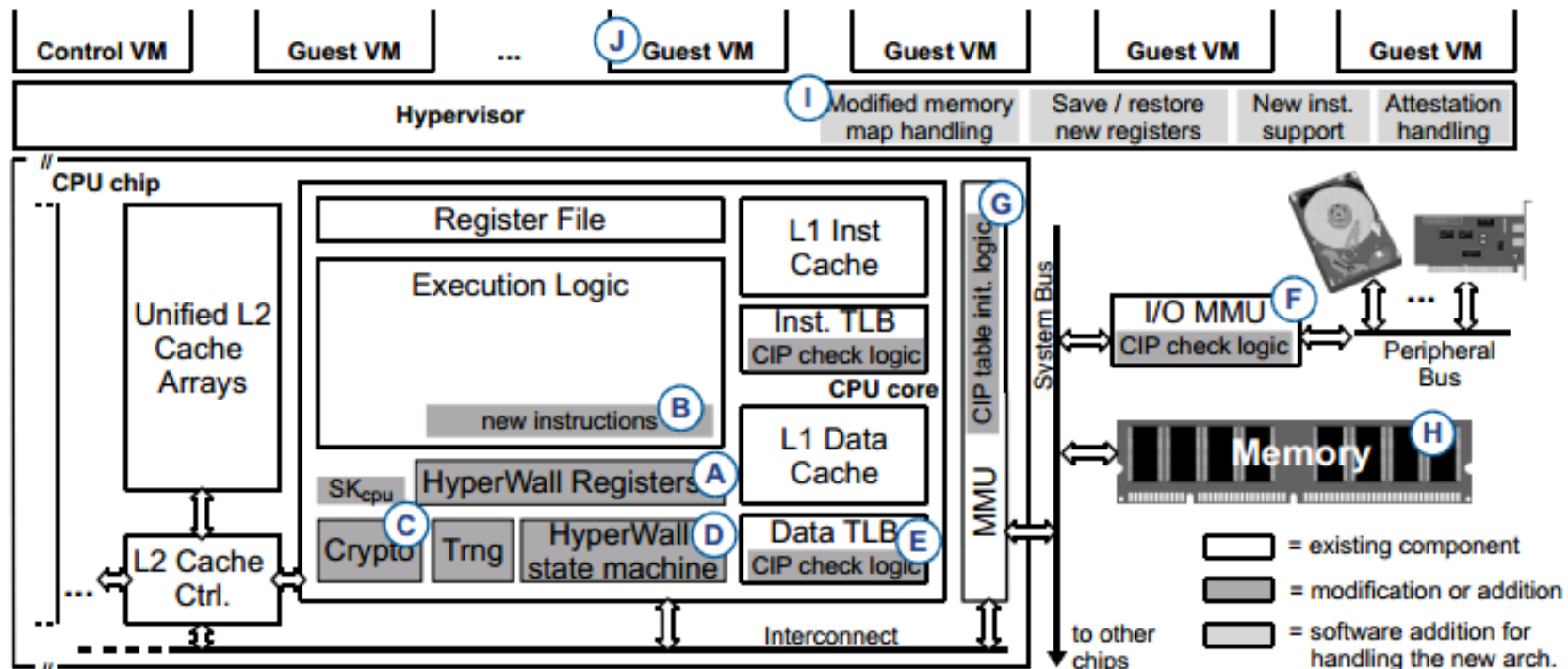
The VMM performs transparently VM scheduling and resource allocation.

Benefits

Stronger security and better performance than software solutions

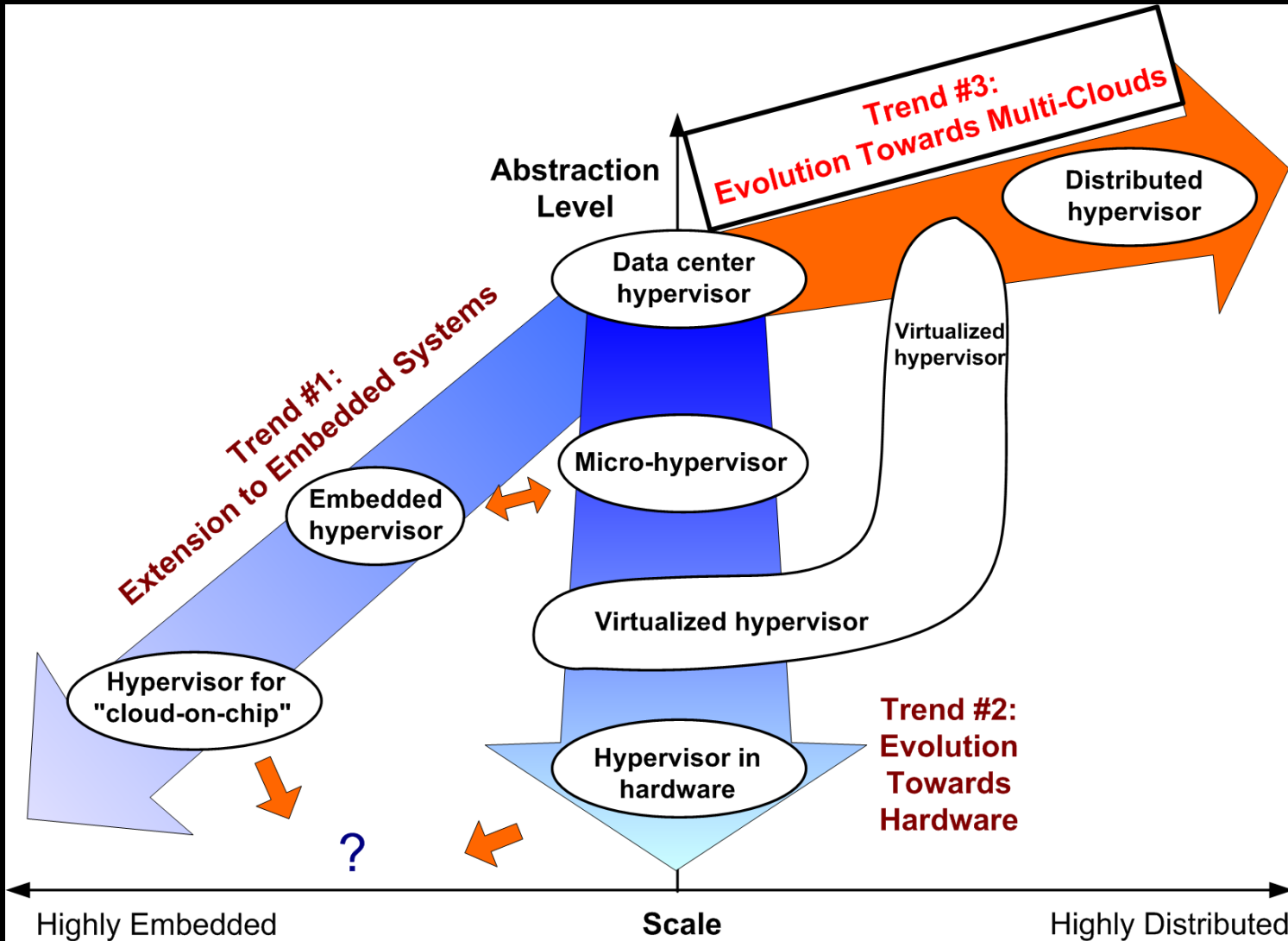
Cost might no longer be a barrier:

- Changes in micro-architecture are fairly small.
- Providers might pay for extra assurance level.



Source: J. Szefer and R. Lee, Architectural Support for Hypervisor-Secure Virtualization, ASPLOS, 2012.

Disruption #3: Evolution Towards Multi-Clouds



Towards User-Centric Clouds

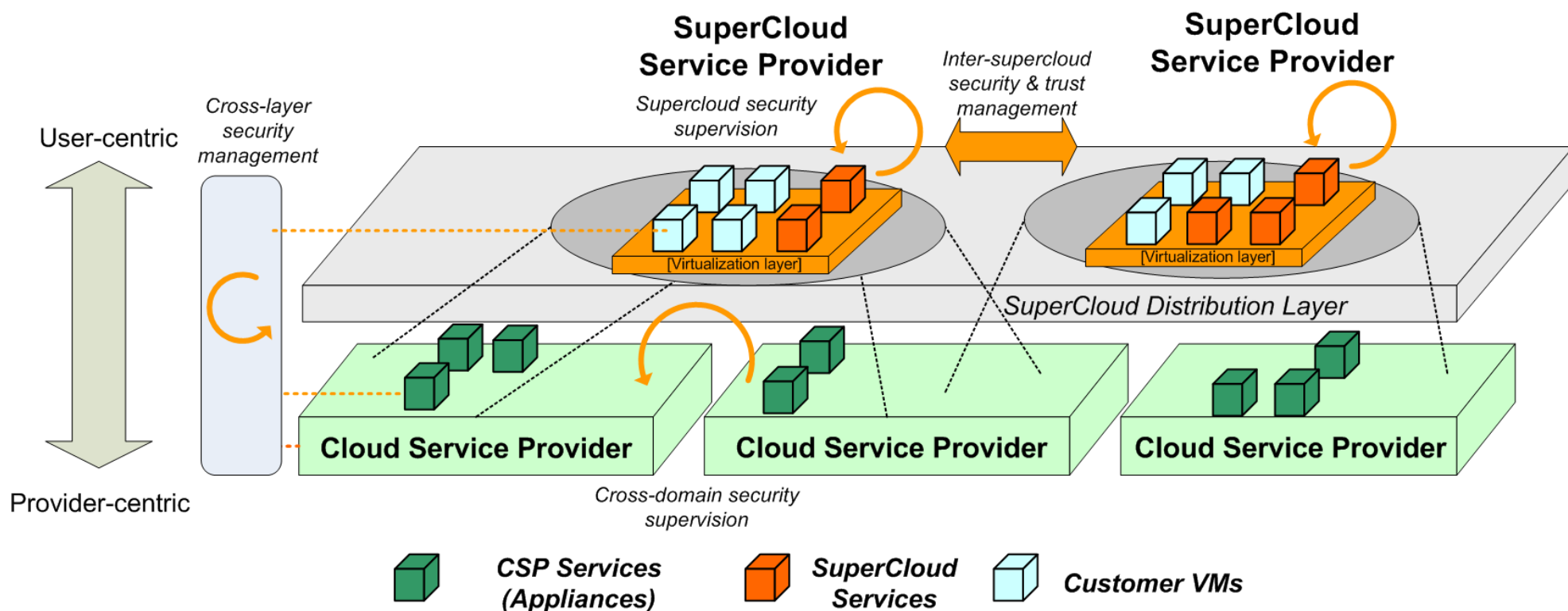
Provider-centric cloud deficiencies

- **Lack of unified control:**
vendor lock-in, monolithic infrastructures
- **Lack of interoperability:**
for infrastructure services

Towards User-Centric Clouds

User-centric clouds (super-clouds)

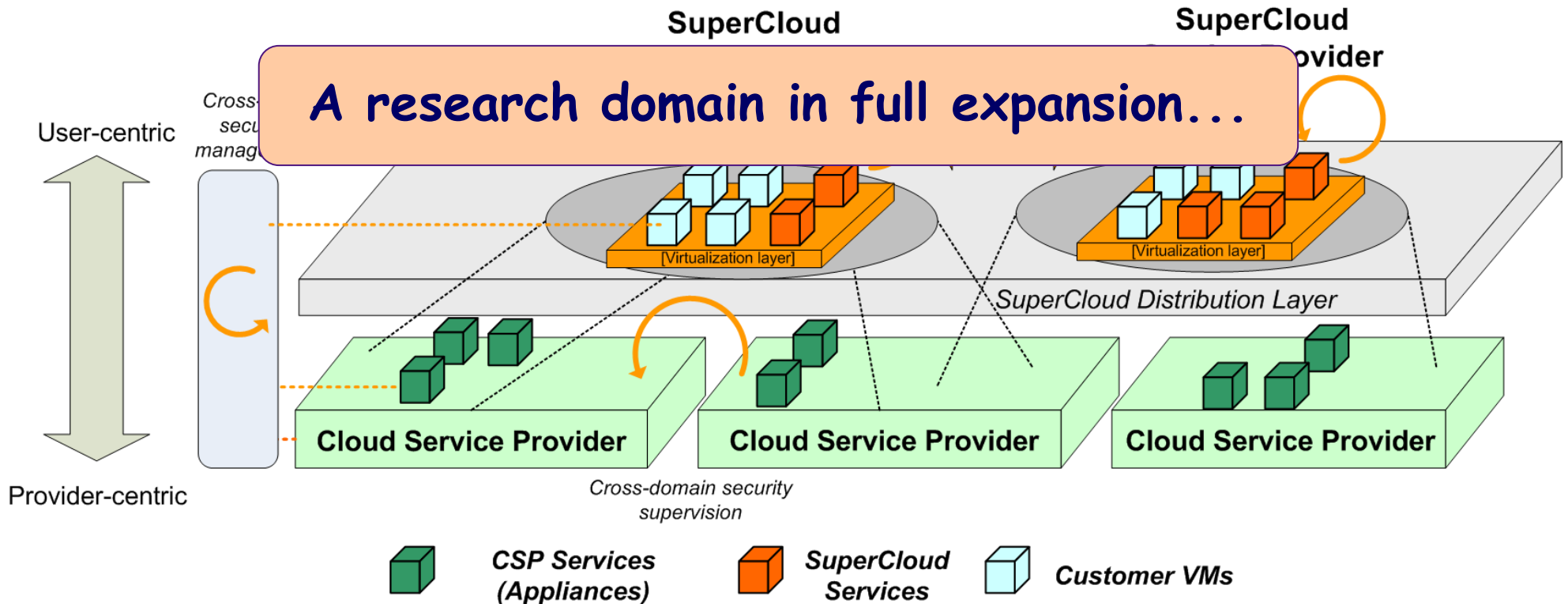
- **Cloud resource distribution plane.**
- **Benefits:**
 - ✓ Independence from provider.
 - ✓ Increased customizability.
 - ✓ New business opportunities.



Towards User-Centric Clouds

Towards fully distributed hypervisors...

- Split infrastructure into provider- / user-controlled domains/modules.
- Some design alternatives:
 - ✓ Extensible hypervisors [« Unshackle the Cloud! », HotCloud'11].
 - ✓ Modular management interface [« Towards Self-Service Clouds », CCS'12].
 - ✓ Nested virtualization [XenBlanket, EUROSYS'12; Inception, USENIX ATC'13].



Perspectives

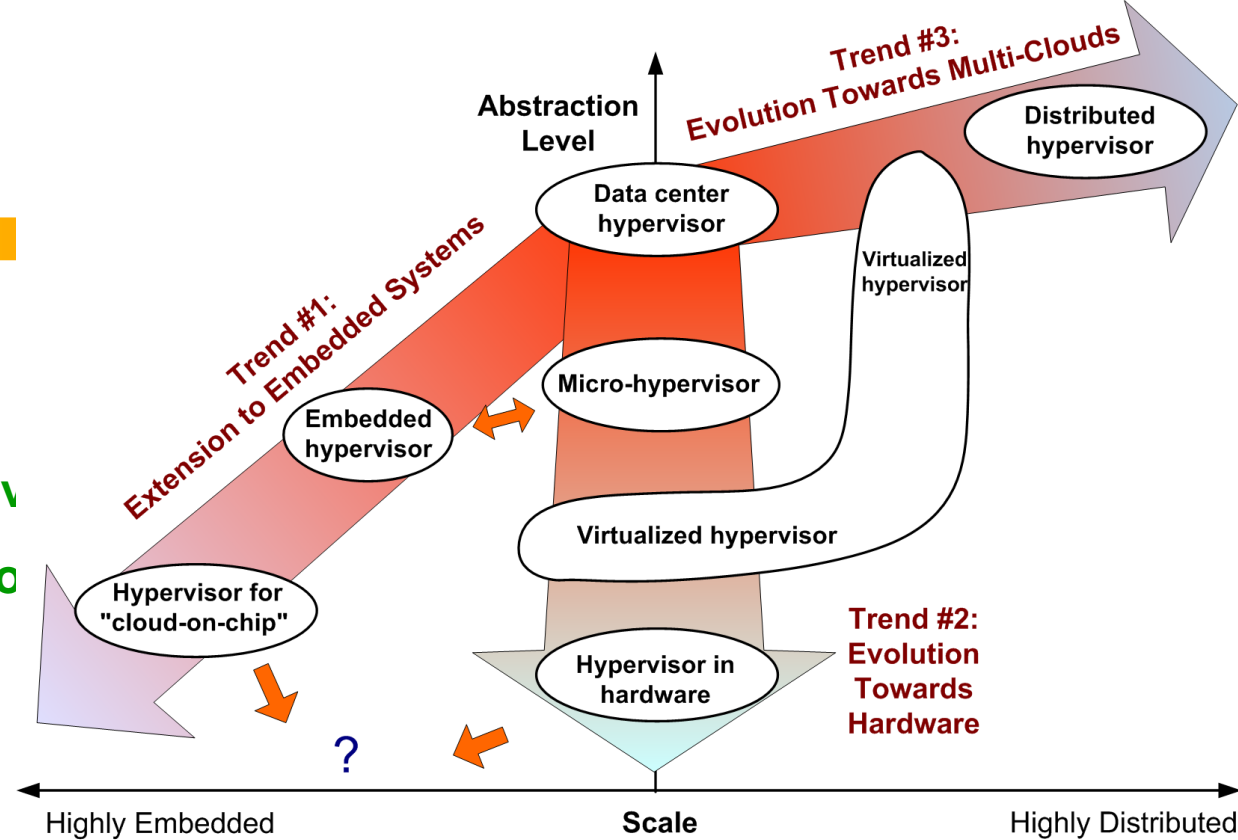


- ▶ **Exploitation of virtualization vulnerabilities** are some of the most serious cloud threats, making the **hypervisor** a keystone component of cloud security.
- ▶ **Looking back...**

Perspectives



- ▶ **Exploitation of virtualization v threats, making the hypervisor**
- ▶ **Looking back...**



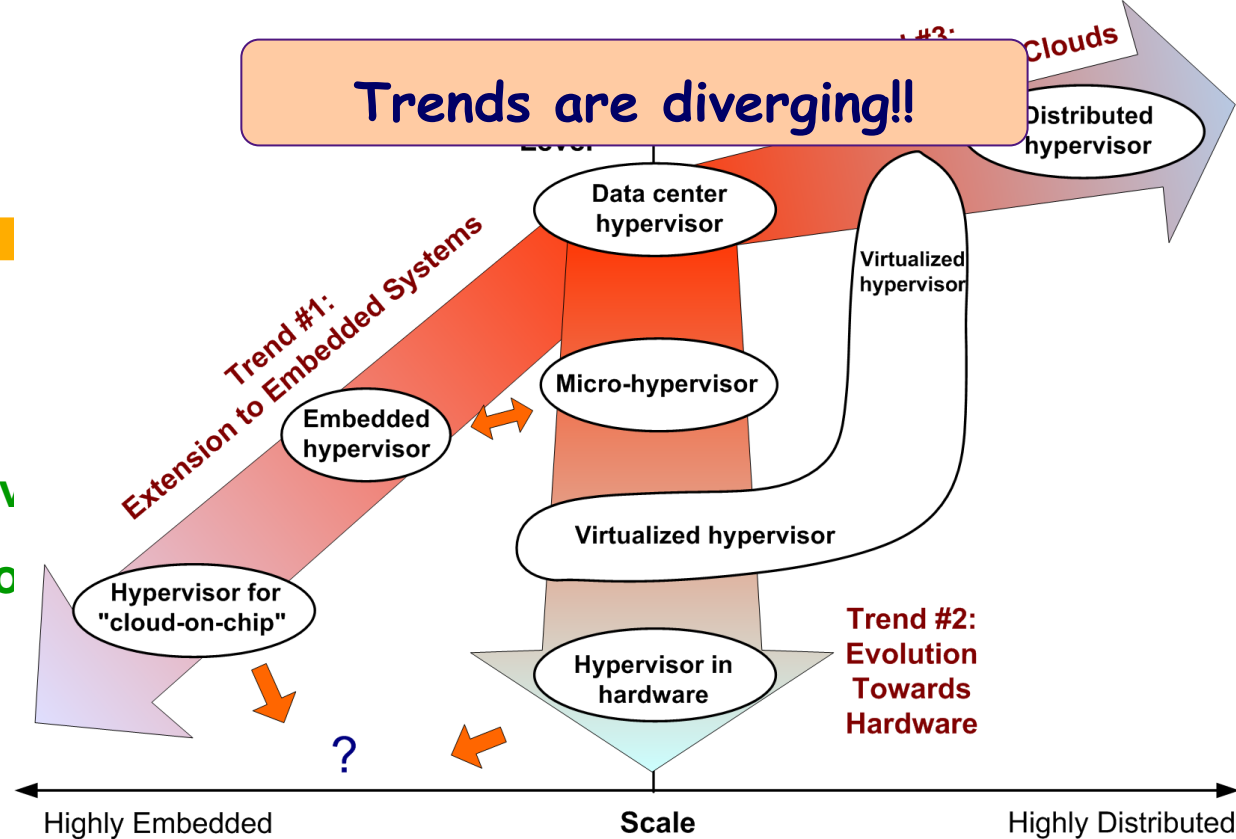
- The main challenges are **rising infrastructure complexity** and **rapid threat evolution**.
- Mechanisms are not well integrated. New architectures are promising but far from mature.
- Two ultimate goals are **cross-layer protection** and **end-to-end security**.

Perspectives

Static Cloud Security

Flexible Cloud Security

- ▶ **Exploitation of virtualization vulnerabilities**, making the hypervisor a security threat
- ▶ **Looking back...**



- The main challenges are **rising infrastructure complexity** and **rapid threat evolution**.
- Mechanisms are not well integrated. New architectures are promising but far from mature.
- Two ultimate goals are **cross-layer protection** and **end-to-end security**.
- As virtualization expands, **not one but multiple** « good » security architectures.

⇒ **A fast moving research domain...**

⇒ **...critical to monitor to protect future cloud systems.**

Thanks!

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